FIG. 1 PRIOR ART

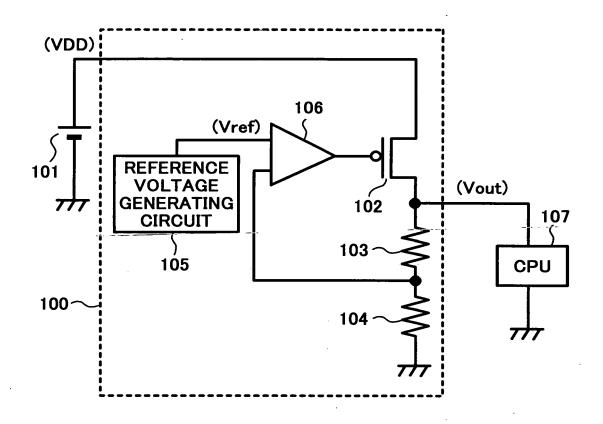


FIG. 2 PRIOR ART

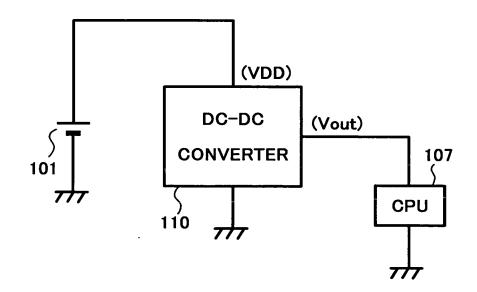


FIG. 3

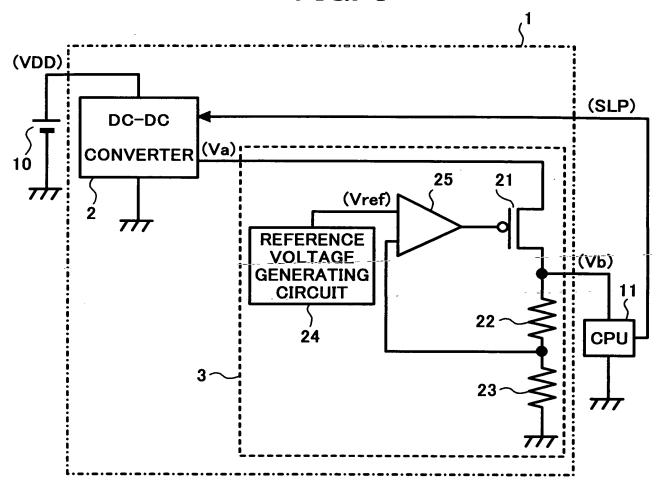


FIG. 4

VDD

31

CONTROLLER

(SLP)

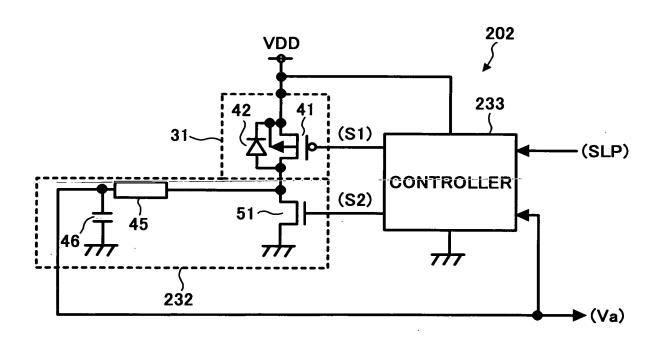
46

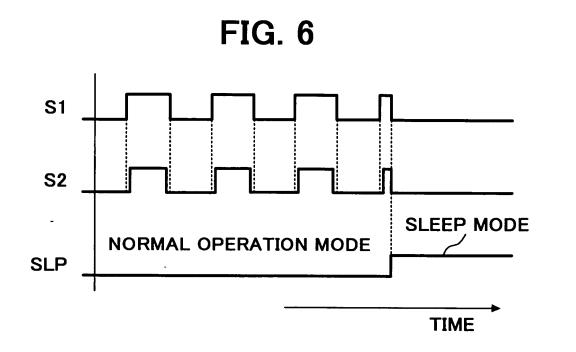
777

32

(Va)

FIG. 5





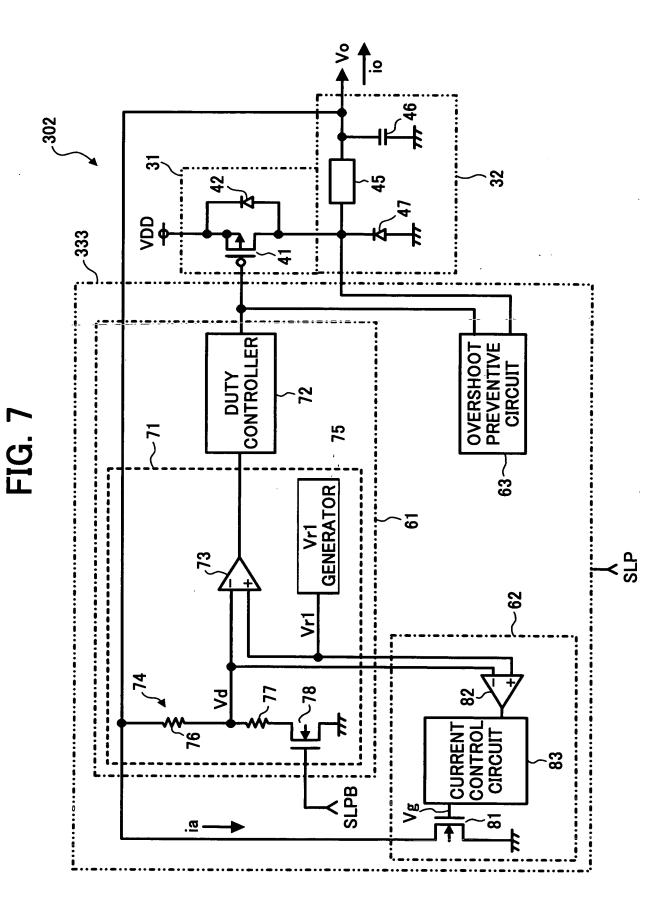


FIG. 8

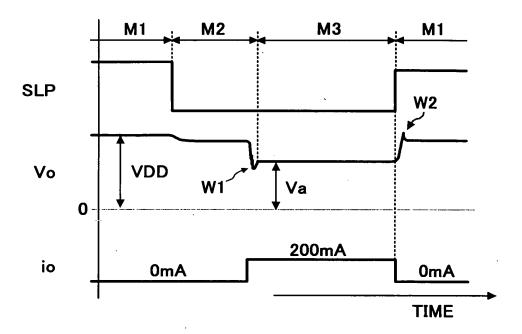


FIG. 9

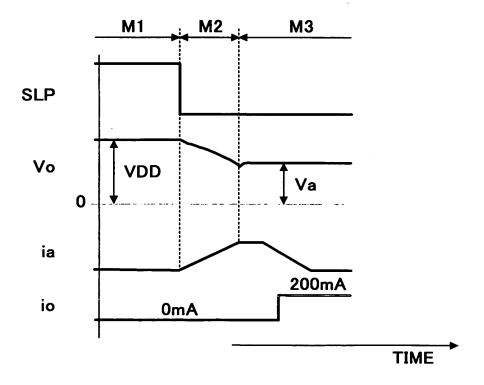


FIG. 10

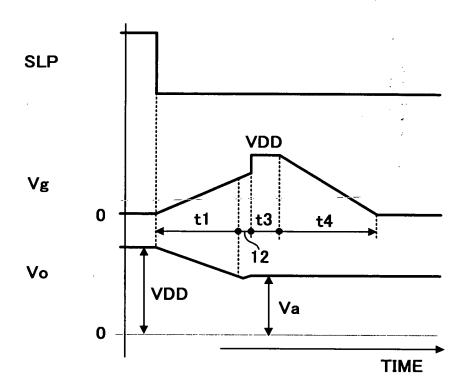


FIG. 11

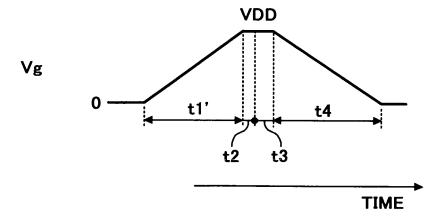


FIG. 12

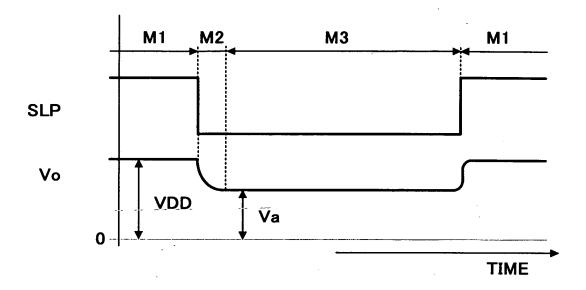
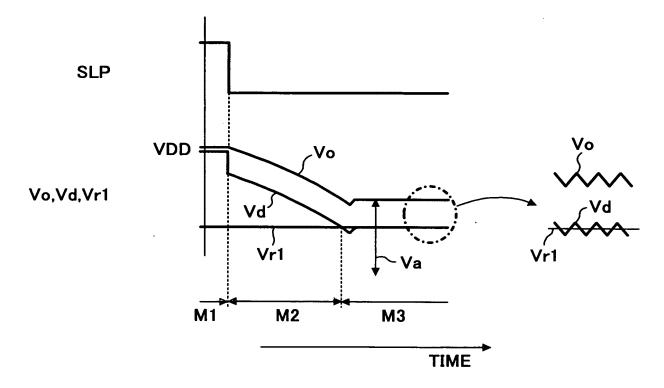


FIG. 13



**1**.º 32 **₽** 433 DUTY OVERSHOOT PROTECTION CIRCUIT -91 63~ GENERATOR GENERATOR ر 92 Vr2 **61a** Vr1 SWITCHING CIRCUIT SLP ^ Vr2 | -62 K **P** CURRENT CONTROL CIRCUIT 83 SLPB <u>.a</u>

FIG. 14

FIG. 15

